



10/665751

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vishnu K. Agarwal et al.

Examiner: Hoai V Pham

Patent No.: 7,282,756

Group Art Unit: 2814

Issue Date: October 16, 2007

Docket No: 1998-0922.02/US

Title: STRUCTURALLY-STABILIZED CAPACITORS AND METHOD OF MAKING OF SAME

ATTN: Certificate of Correction Branch  
Commissioner for Patents  
Office of Patent Publication

We are transmitting herewith the attached:

**Certificate**

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

NOV 07 2007

**of Correction**

**Please charge any additional fees or credit overpayment to Deposit Account No. 13-3092.**

**Customer Number: 26809**

Respectfully submitted,

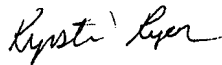
Date: November 1, 2007

By: 

Russ Slifer  
Reg. No. 39,838

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Kyrstin Ryan  
Name

  
Signature

NOV 7 2007

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SAME

**REQUEST FOR CERTIFICATION OF CORRECTION**

ATTN: Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.


**As the error is that of the Patent Office, it is believed that no fee is due.**

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

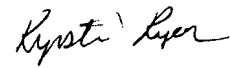
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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,282,756

Page (1) of 1

DATED : October 16, 2007

INVENTOR(S) : Agarwal et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, line 52, in Claim 10, delete "of the;" and insert - - of the microstructures; - -, therefor.

In column 12, lines 16-17, in Claim 15, delete "semiconductor;" and insert - - semiconductor substrate; - -, therefor.

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